

Fig.1A

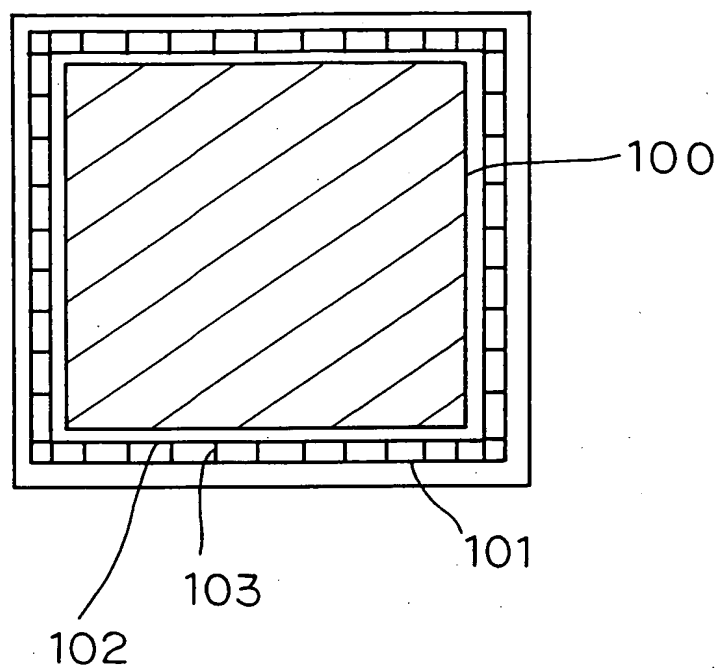


Fig.1B

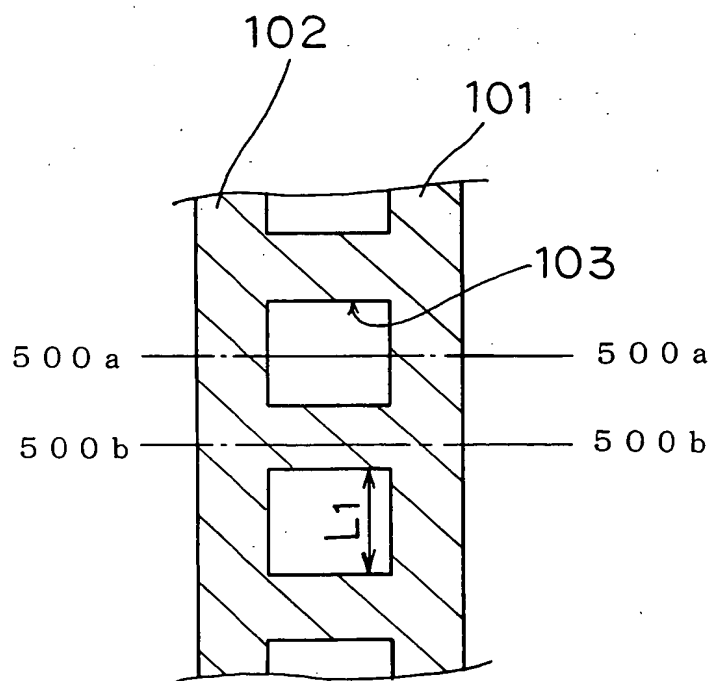


Fig.2A

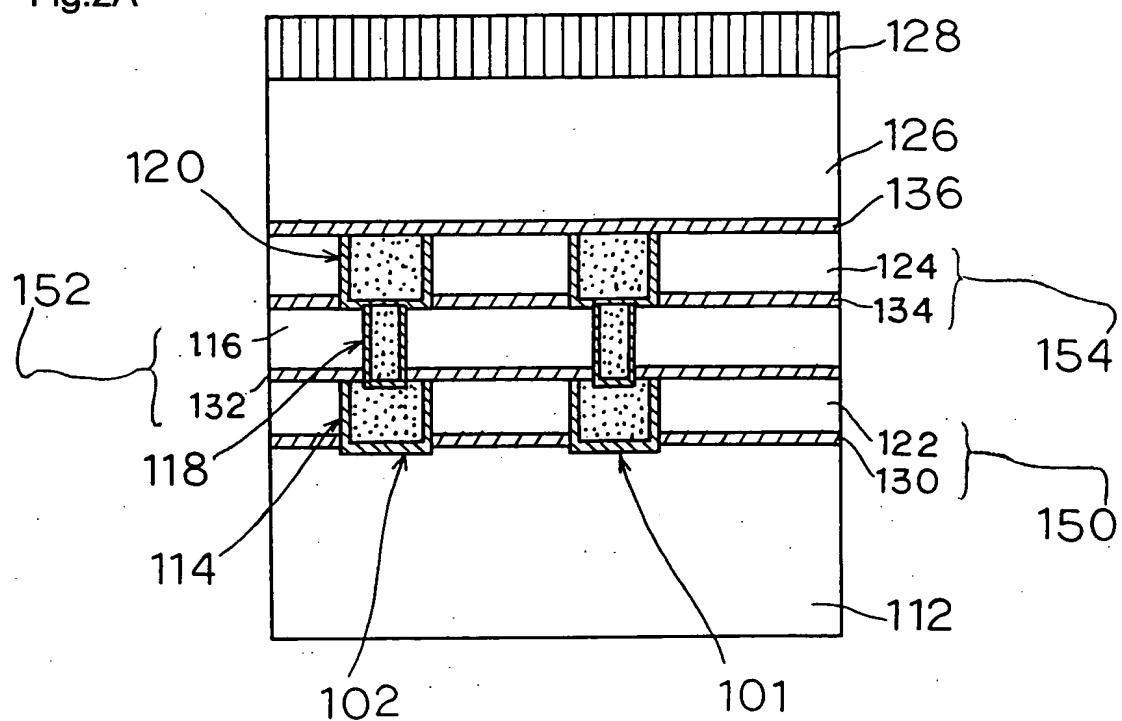
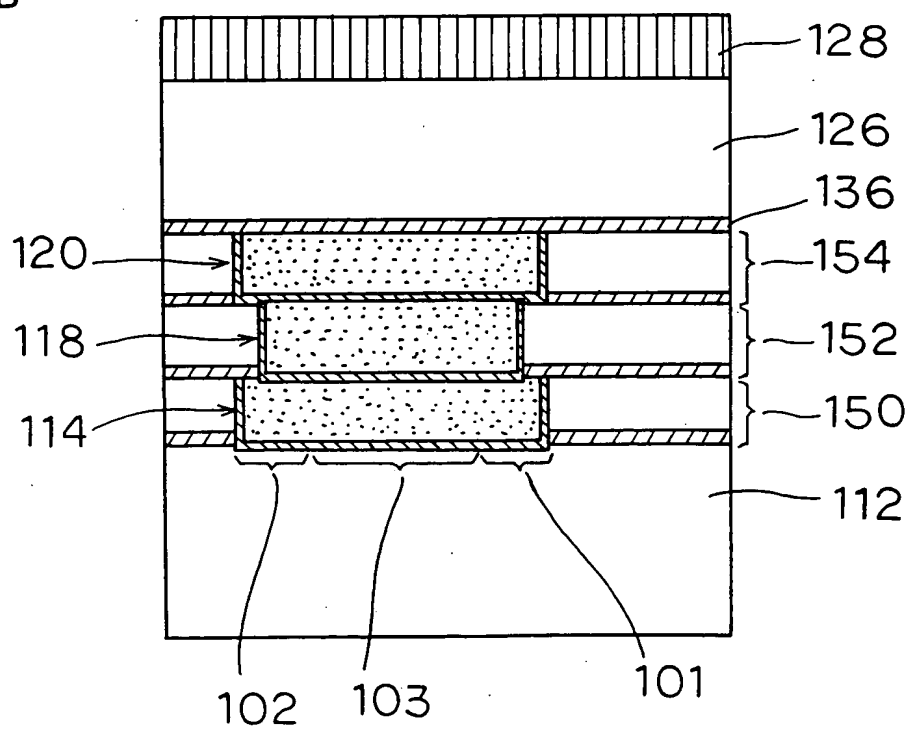
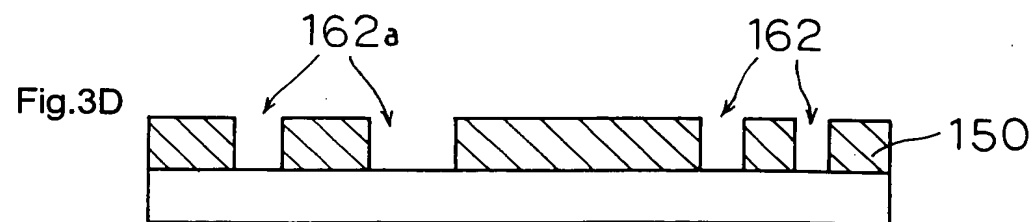
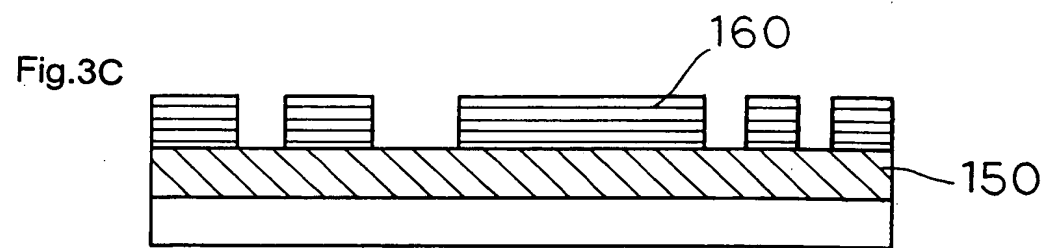
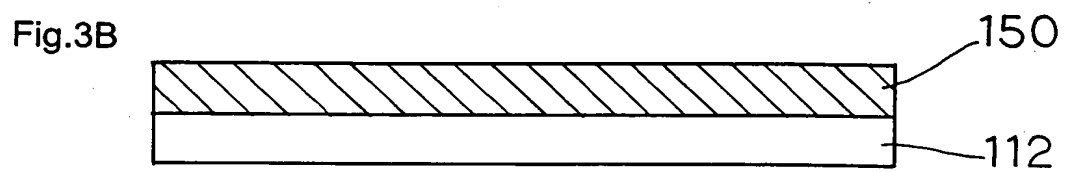
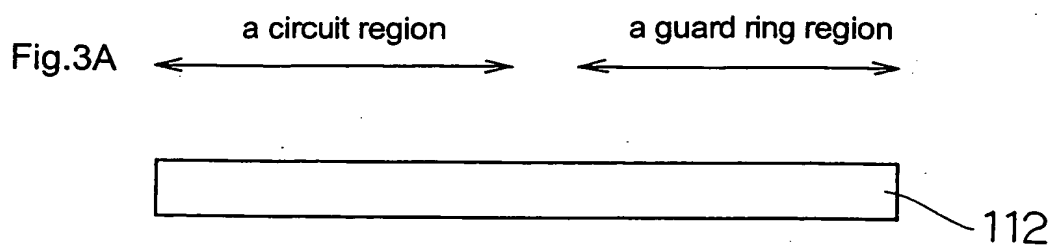
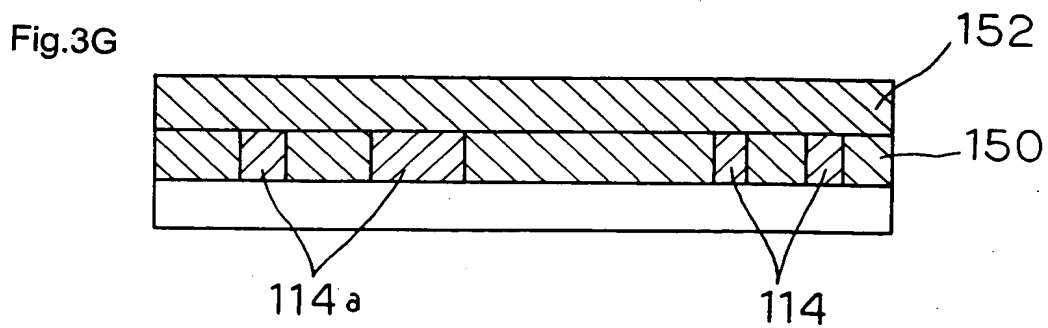
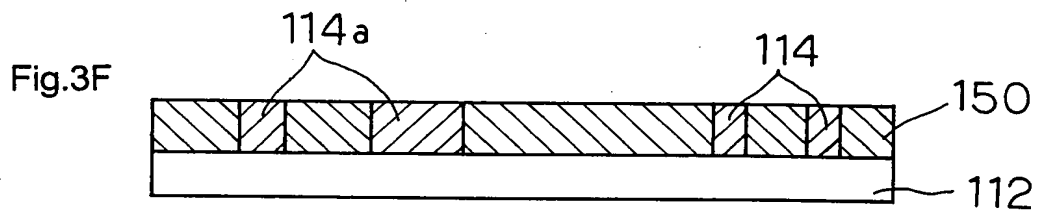
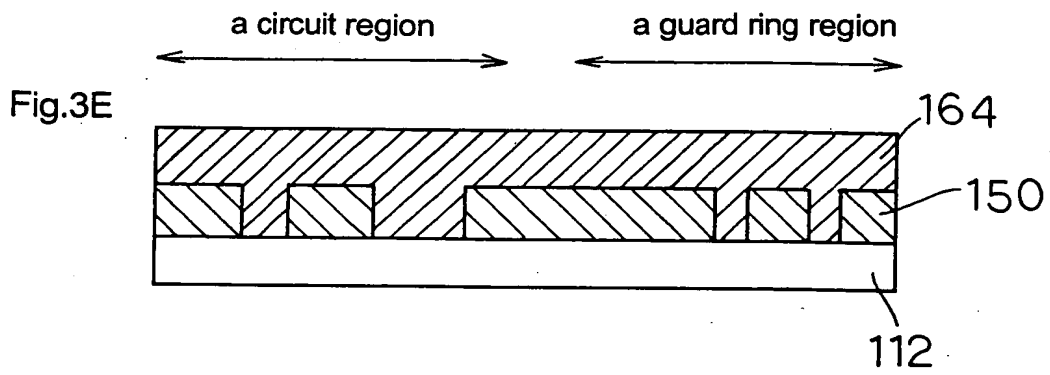
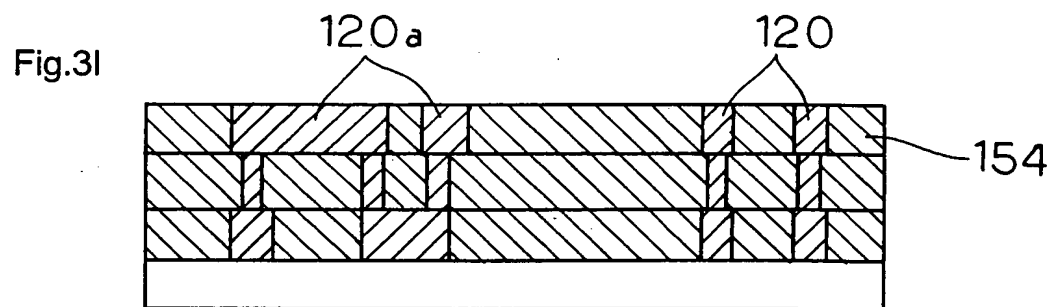
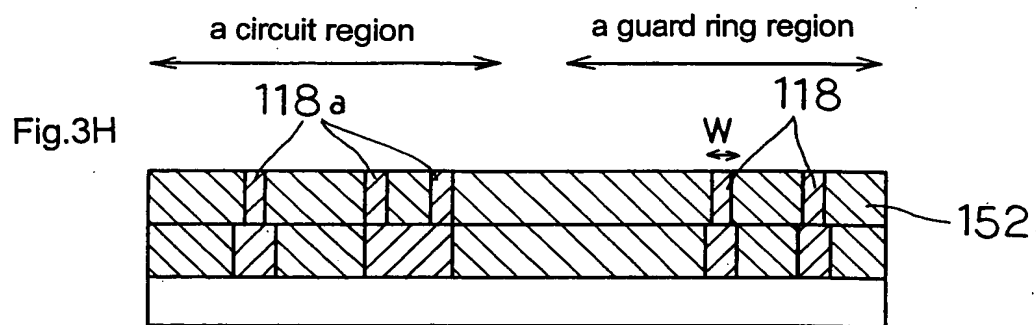


Fig.2B









a circuit region      a guard ring region

Fig.4A



Fig.4B

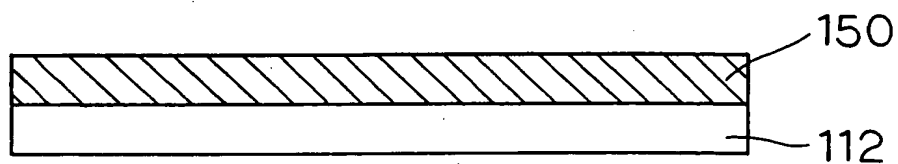


Fig.4C

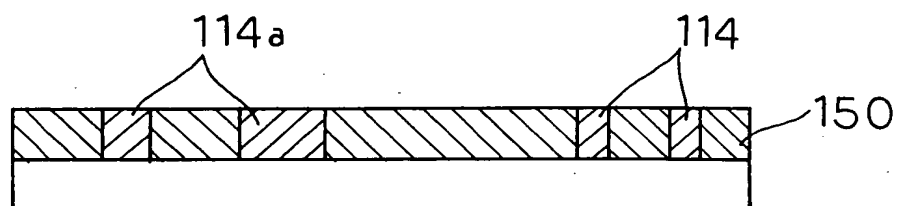
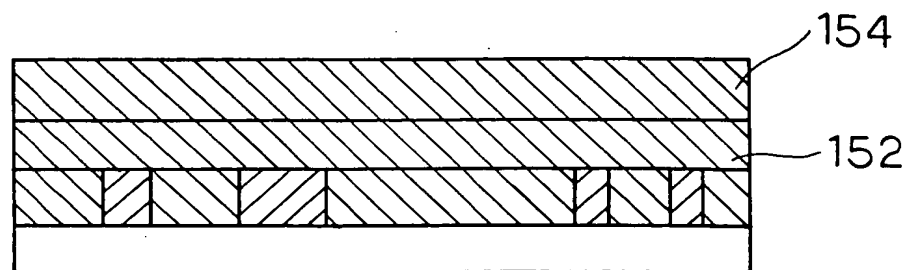


Fig.4D



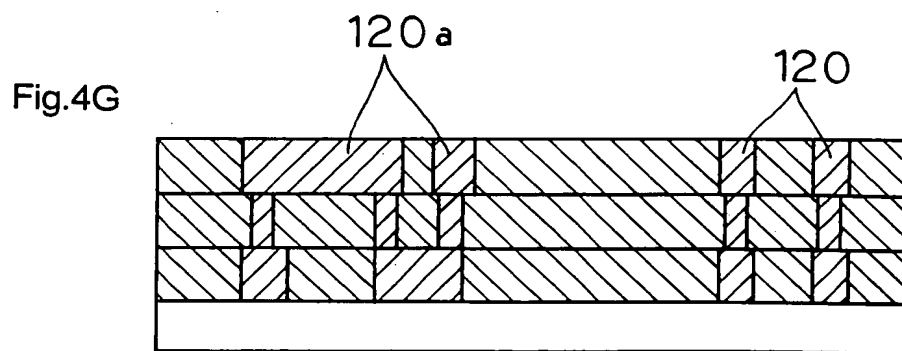
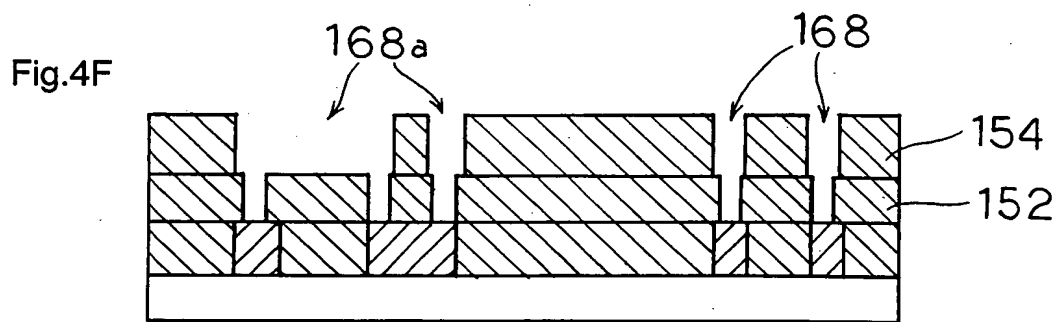
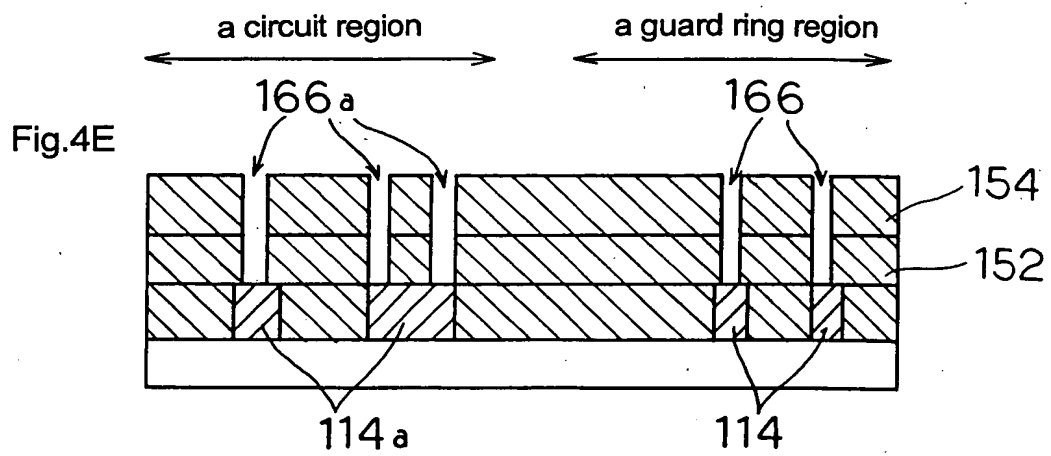


Fig.5

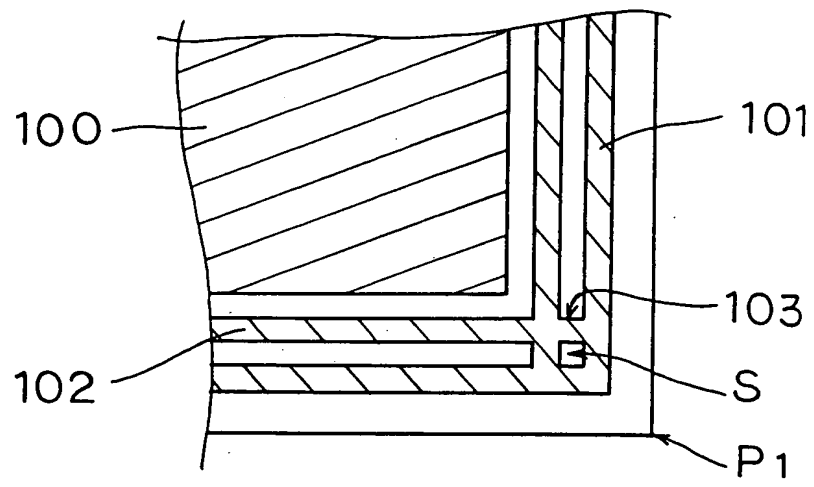


Fig.6

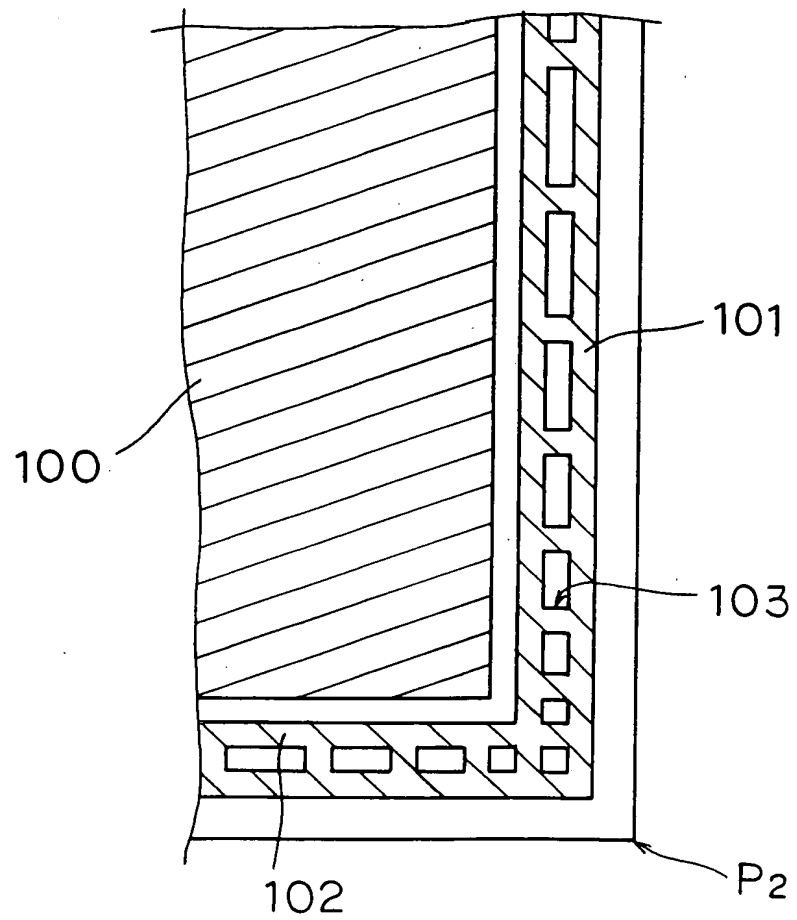




Fig.7A

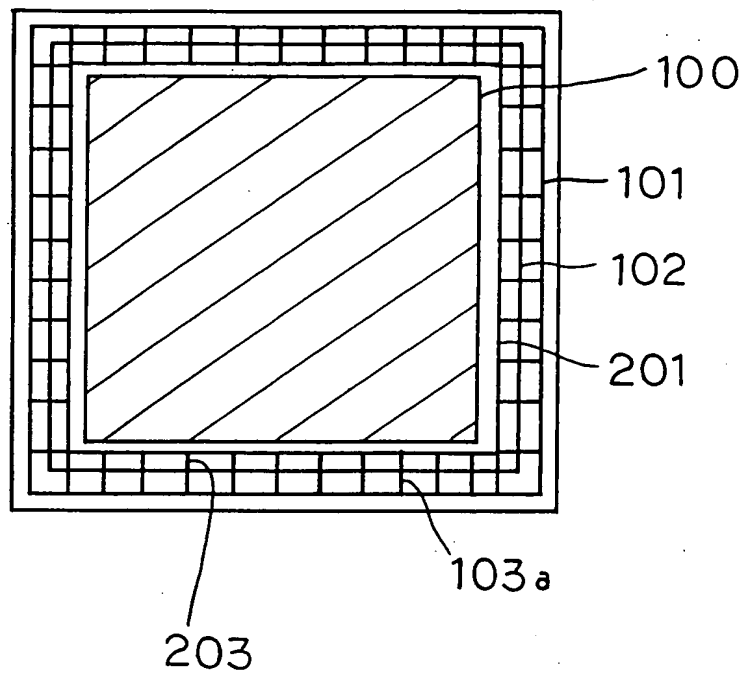


Fig.7B

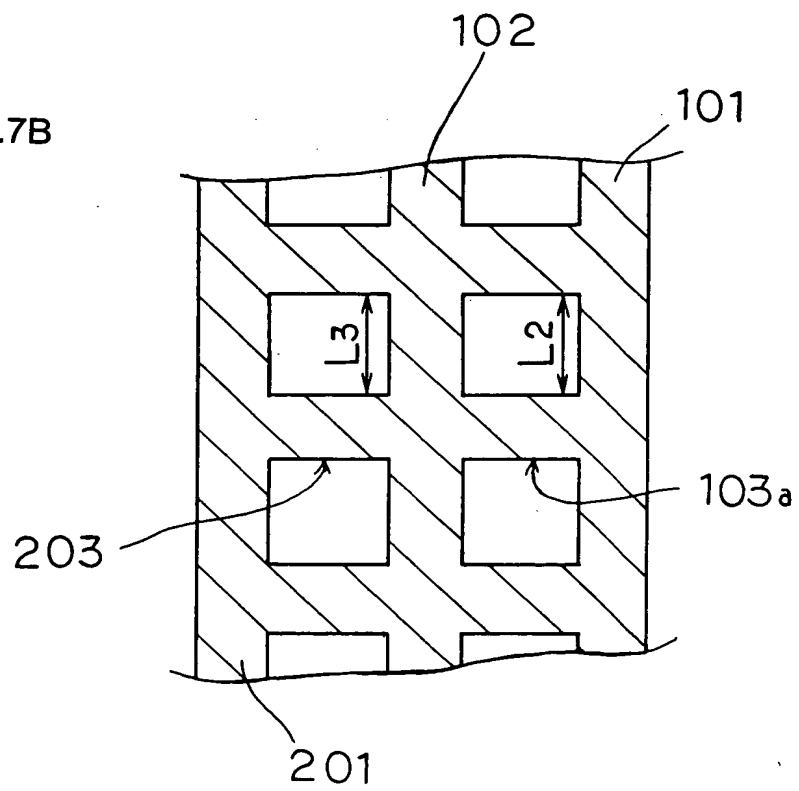


Fig.8A

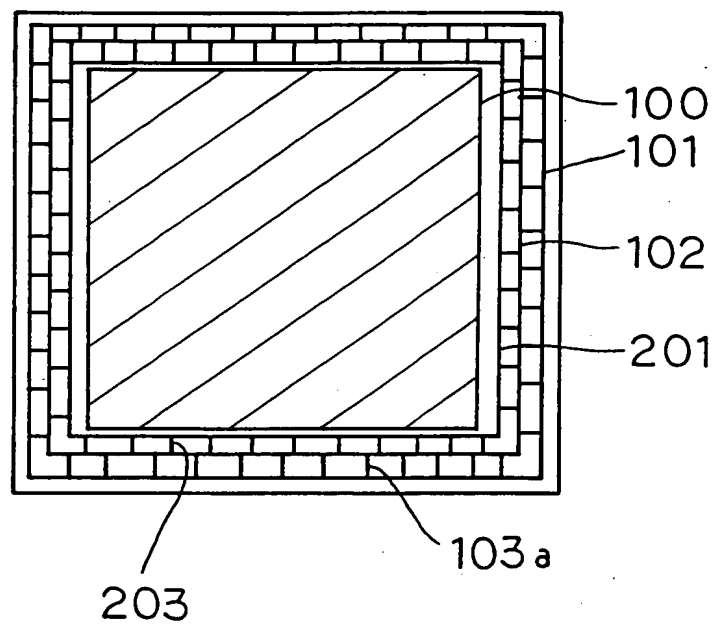


Fig.8B

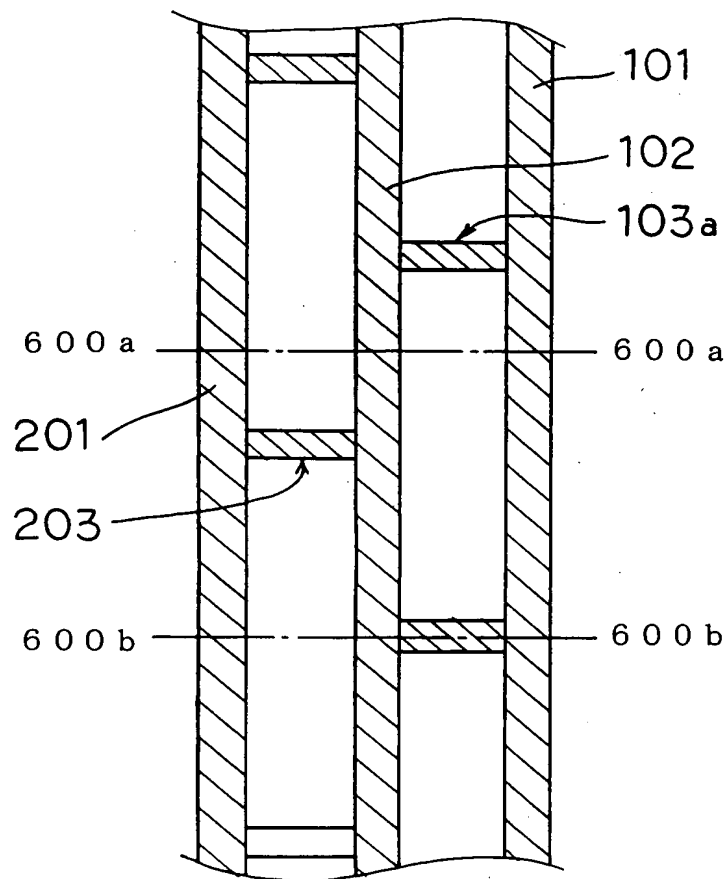


Fig.9A

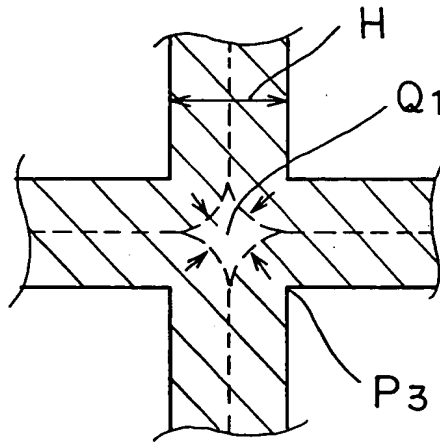
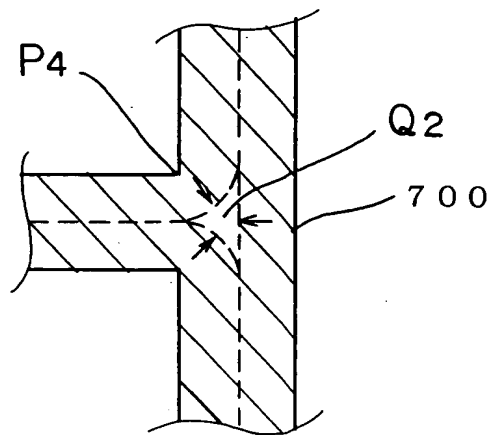


Fig.9B



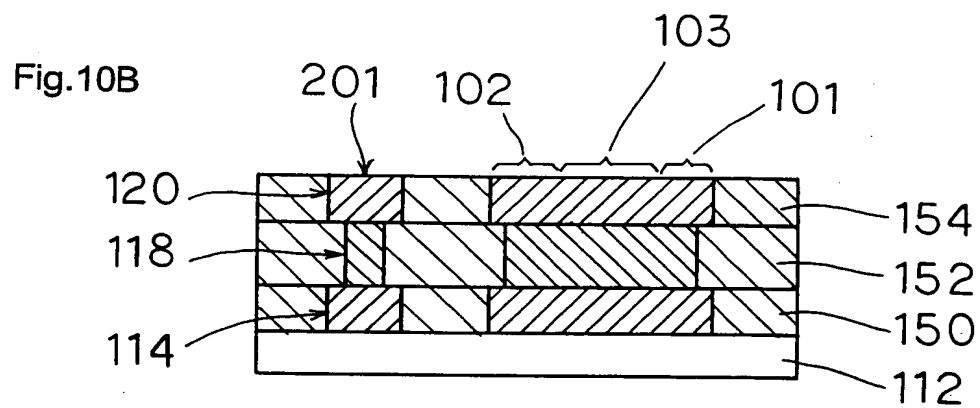
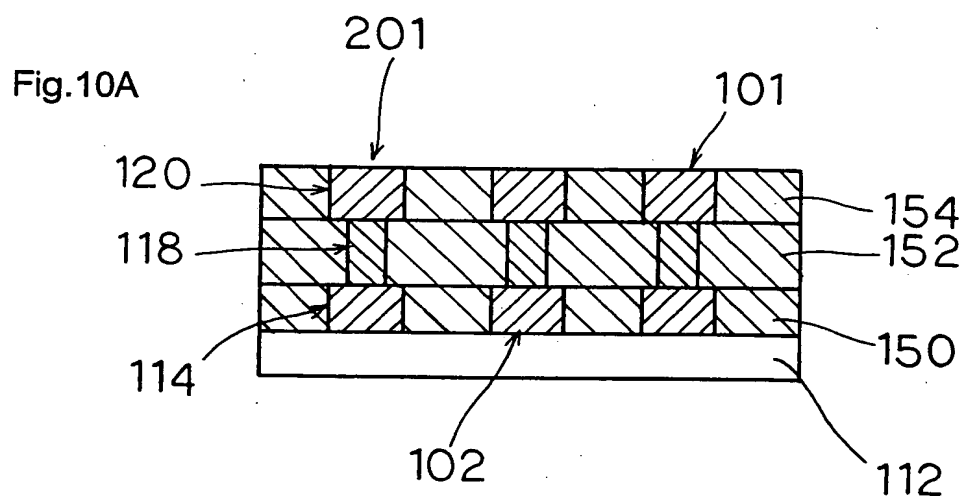


Fig.11A

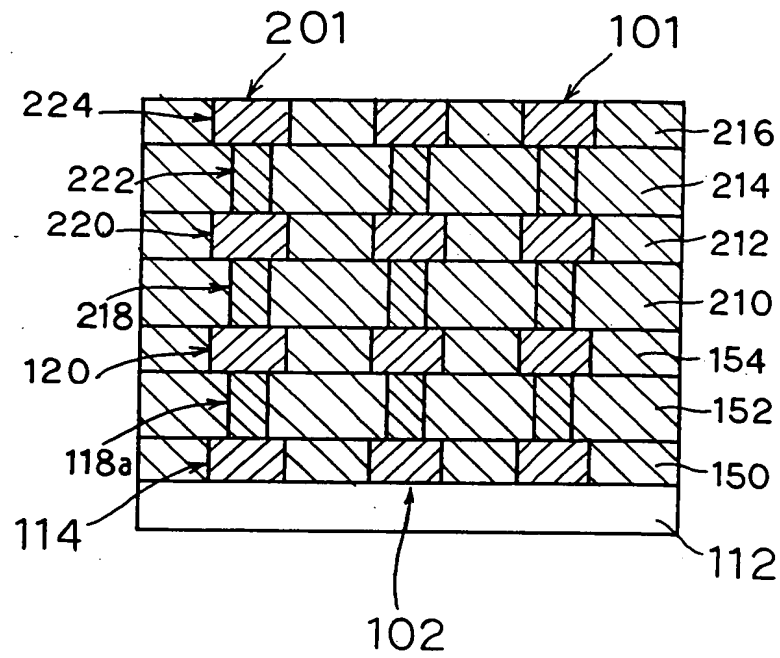


Fig.11B

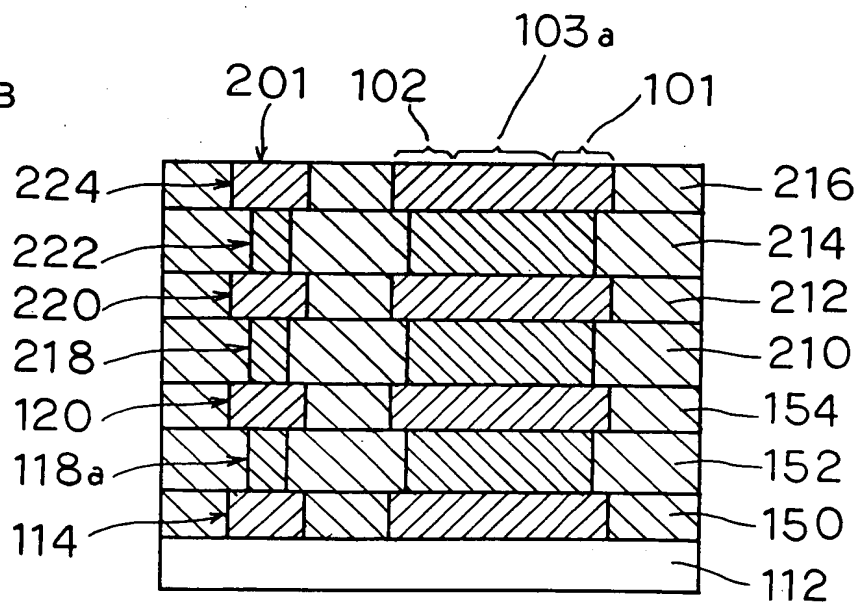


Fig.12

